# EFFICIENT OPERATIONS IN LARGE FINITE FIELDS FOR ELLIPTIC CURVE CRYPTOGRAPHIC 



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#### Abstract

An efficient method to compute the finite field multiplication for Elliptic Curve point multiplication at high speed encryption of the message is presented. The methods of the operations are based on dynamic lookup table and modified Horner rule method. The modified Horner rule method is not only to finite field operations but also to Elliptic curve scalar multiplication in the encryption and decryption. By comparison with using Russian Peasant method and in the new proposed method, one of the advantages of utilizing the proposed algorithm is that in the Elliptic Curve point addition are reduced by a factor of three in GF (2163). Therefore, using the Algorithm 1 running on Intel CPU, computation cost of the multiplication method is above $70 \%$ faster than using standard multiplication by Russian Peasant method. Ultimately, the proposed Algorithm 1 for evaluating multiplication can be made regular, simple and suitable for software implementations.


## 1. INTRODUCTION

In theory, a finite field is an algebraic structure with established operations for addition, subtraction, multiplication, and division by satisfying an Abelian group. These operations have following four properties closure, associativity, commutativity, and having an inverse element. Galois Fields GF (2m) have a wide variety of applications utilized in the cryptographic standards of ANSI and error correction code (Chen authors, 2016). The industry uses Elliptic curve groups over the large finite fields of GF(2m) and GF(p), Koblitz EC groups in GF(2m) (Koblitz, 1987) faster than $\operatorname{GF}(p)$. The operation modulo is using binary irreducible polynomial in finite field (Scott, 2007; Hasan authors, 1992) that is suitable for resource-constrained systems, such as cellphone, networked wireless sensors, and smart cards. The most efficient and secure cryptographic system in use today is known as elliptic curve cryptography (ECC) and is based on the concept of elliptic curves built over Galois fields (Savas \& Ko_c, 2010). NIST recommended curves: Koblitz GF( 2 m ), where m is $163,233,283,409$, and 571 . Elliptic curves are a type of cubic equation of the form $y^{2}=x^{3}+a x+b$, where $a$ and $b$ represent coefficients. When elliptic curves are operation in Galois Field, the points on the curve can be form an Abelian group making it operations to addition of two points on the curve, or the point doubling. ECC encryption and decryption data in Galois Field that is popular the form $y^{2}+x y=x^{3}+a x^{2}+b$, where $a$ and $b$ represent coefficients. The operation on elliptic curves is scalar multiplication (Ansari \& Hasan, 2011) which refers to multiplying a Point P by an integer k , resulting in the Point $\mathrm{k}^{*} \mathrm{P}$ scalar multiplication not only dominates the

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execution time of ECC algorithms, but it is also essential to the security in systems. In GF ( 2 m ), the addition and the subtraction are the same XOR instruction in the processor. However, both multiplication and inversion operations are complicate in cryptographic systems; they are due to finite field of size m (Kobayashi \& Takagi, 2008; Jing authors, 2006; Luo authors, 2012; Wang authors, 1983; Mahboob \& Ikram, 2005; Brwon, 1971). The multiplication and inverse are required while using the Diffie-Hellman key exchange protocol on an elliptic curve (Dong \& Li, 2008), as specified in ANSI X9.62 is required many multiplications and inverses. Therefore, to develop efficient arithmetic operations have high-speed computation that needs for using available technologies. In the past, the multiplication algorithm can be used to look-up tables, which have proposed in (Mahboob \& Ikram, 2005). The lookup table method is pre-compute to reduce the number of operations required during the computation through the pre-computation and to reduce the effective computation time for multiplication by suitable width of the registers of the processor to achieve higher computation speed. The new algorithm has two properties: First, it utilizes the dynamic lookup table by precomputing input data and save memory. Second, it uses modified Horner rule for iteration loop and can determine the table entries quickly. The operation of inverse usually utilizes the polynomial modular mathematic called Euclidean algorithm (Brwon, 1971; Dong \& Li, 2008), in which is hard to be known the worst cases execution time. The Fermat's Little Theorem also uses to compute inverse because the worst cases execution time can found. The adaptation of Itoh-Tsujii method (Guajardo \& Paar, 2002) for standard basis, particularly for Optimal Extension Fields has been effective in achieving fast inversion. However, despite recent improvements, inversion is still the slowest operation in elliptic curve implementations (Agnew authors, 1993; Choi authors, 2002; Kumar, 2006). Finial, this issue is addressed by proposing multiplication methods that the execution time is faster than which measured for others multiplication execution time and it can be speed up establish a shared security over an insecure channel, Elliptic curve Diffie-Hellman (ECDH) (Diffie \& Hellman, 1976).

## 2. PRELIMINARIES

Let $A(x)=\sum_{i=0}^{m-1} a_{i} x^{i}$ and $B(x)=\sum_{i=0}^{m-1} b_{i} x^{i}$ be polynomial over the finite field $G F\left(2^{m}\right)$, where $a_{i}, b_{i} \in\{0,1\}$. The multiplication in the finite field is defined as follows:

$$
\begin{equation*}
C(x) \equiv A(x) \cdot B(x) \bmod F(x) \tag{1}
\end{equation*}
$$

where the polynomial $F(x)$ is the irreducible polynomial. There is common method utilizing Horner rule for computing multiplication, which is rewritten (1) in the following form, where the $B(x)$ polynomial is represented as binary vector $B=\left(b_{m-1}, b_{m-2}, \ldots, b_{0}\right)$, where $b_{i} \in G F(2)$.

$$
\begin{align*}
& C(x) \equiv\left(\cdots\left(\left(a_{m-1} B\right) x+a_{m-2} B\right) x+\cdots+a_{1} B\right) x \\
& +a_{0} B \bmod F(x) . \tag{2}
\end{align*}
$$

The ECC with multiplication is irreducible polynomial $f=x^{m}+x^{m-1}+\cdots+x^{2}+x+1$. In (1), the Russian Peasant method can be written as a function in C programming as follows:

```
Russian Peasant method
GFM(a, b){
c = 0;
cbit=1<<m;
for i=0;i<m; i=i+1
    if (b & 1)
        c^= a;
    if (a & cbit)
        a=(a<< 1)^f;
    else
        a <<= 1;
```

```
b >>= 1;
}
return c;
}
```

The inverse element of $A$ in finite field $G F\left(2^{m}\right)$ is derived by Fermat's Little Theorem, which is given by

$$
\begin{equation*}
A^{-1}=A^{2^{m}-2}=A^{2^{m-1}+2^{m-2}+\cdots+2} . \tag{3}
\end{equation*}
$$

Many multiplications are required in common method of the inverse, which need $2^{m}$ - 3 multiplications for calculating inverse element. In others word, it need more computation time for computing inverse but using number theory can reducing multiplication that is only $m-1$ multiplications. The inverse operation also can use the extended Euclidean algorithm.

$$
\begin{equation*}
F(x) s(x)+A(x) t(x)=1, \text { where } A(x) t(x) \equiv 1 \bmod F(x) \tag{4}
\end{equation*}
$$

Thus, the remainder of division $t(x)$ by $\bmod F(x)$, where $F(x)$ is irreducible polynomial, that is the multiplicative inverse of $A(x) \bmod F(x)$. The compute the inverse element over finite field $G F\left(2^{m}\right)$ and the extended Euclidean algorithm also can use for computing multiplicative inverse in finite field. However, Euclidean division operation is hard to know the maximum execution time when the number is large. Points $\mathrm{P}=\left(x_{1}, y_{1}\right)$ and $\mathrm{Q}=\left(x_{2}, y_{2}\right)$ on the curve, assume $x_{1} \neq x_{2}$. Let $y=\lambda x+d$ be the line that intersects P and Q , the value of the $\lambda$ slope needs calculating inverse.

$$
\begin{align*}
& f(x, y)=y^{2}+x y=x^{3}+a x^{2}+b \\
& E_{2^{m}}(a, b) \rightarrow f(x, y), G F\left(2^{m}\right) \\
& \text { If } \neq Q, P+Q=R=\left(x_{3}, y_{3}\right) \\
& \lambda=\frac{y_{2}+y_{1}}{x_{2}+x_{1}} \\
& x_{3}=\lambda^{2}+\lambda+x_{1}+a  \tag{5}\\
& y_{3}=y_{1}+\left(x_{1}+x_{3}\right) \lambda+x_{3} \\
& \text { If } P=Q, P+Q=R=2 P=\left(x_{3}, y_{3}\right) \\
& \lambda=x_{1}+\frac{y_{1}}{x_{1}} \\
& x_{3}=\lambda^{2}+\lambda+a  \tag{6}\\
& y_{3}=y_{1}+\left(x_{1}+x_{3}\right) \lambda+x_{3}
\end{align*}
$$

For example, $G F\left(2^{3}\right), p(x)=x^{3}+x+1$.
$E_{2^{3}}\left(\alpha^{3}, \alpha^{0}\right) \rightarrow f(x, y) \rightarrow y^{2}+x y=x^{3}+\alpha^{3} x^{2}+\alpha^{0}$.
Let $P$ and $Q$ be curve point, where $P=\left(x_{1}, y_{1}\right), Q=\left(x_{2}, y_{2}\right)$.
$R=\left(x_{3}, y_{3}\right)$, the point of $R$ is equal $P$ point addition $Q$ point.
We know that the slope is $\lambda=\frac{y_{2}+y_{1}}{x_{2}+x_{1}}$. The function of line is $y=\lambda x$.

## 3. IMPLEMENTATION OF MULTIPLICATION IN LARGE FIELDS GF( $\left.2^{\mathrm{m}}\right)$.

Using a two-term polynomial $a_{l} x+a_{w}$ is designed for finite field multiplication, where $a_{l}, a_{w} \in G F(2)$. Let $q \equiv m \bmod 2$, where the value of the remainder $q$ is either 0 or 1 . Therefore, the computation of $A(x)=\sum_{i=0}^{m-1} a_{i} x^{i}$ can be replaced by the following equation:

$$
\begin{equation*}
A(x)=\left(\sum_{i=0}^{\lfloor(m-2) / 2\rfloor-1}\left(a_{m-2 i-1} x+a_{m-2 i-2}\right) x^{(m-2 i-2)}+a_{q+1} x+a_{q}\right) x^{q}+q \cdot a_{0}, \tag{7}
\end{equation*}
$$

A substitution of Equation (7) into Equation (1) yields,

$$
\begin{equation*}
C(x) \equiv\left(\left(\sum_{i=0}^{\lfloor(m-2) / 2\rfloor-1}\left(a_{m-2 i-1} B x+a_{m-2 i-2} B\right) x^{(m-2 i-2)}+a_{q+1} B x+a_{q} B\right) x^{q}+q B \cdot a_{0}\right) \tag{8}
\end{equation*}
$$

$\bmod F(x)$.

A two-term polynomial $a_{m-2 i-1} B x+a_{m-2 i-2} B$ can be pre-computed in order to generate a table of values. To see this, let $L\left(a_{l}, a_{w}\right)=a_{l} B x+a_{w}$ be an array as lookup table, where $l=m-2 i-1$ and $w=m-2 i-2$. Therefore, a substitution of $L\left(a_{l}, a_{w}\right)$ into Equation (8) yields $C(x) \equiv\left(\left(\sum_{i=0}^{\lfloor(m-2) / 2\rfloor-1} L\left(a_{l}, a_{w}\right) x^{w}+L\left(a_{q+1}, a_{q}\right)\right) x^{\cdot q}+q \cdot a_{0} B\right)$ $\bmod F(x)$. Note that $\lfloor x\rfloor$ denotes the largest integer less than or equal to $x$. This implies that $\sum_{i=0}^{\lfloor(m-2) / 2\rfloor-1} L\left(a_{l}, a_{w}\right) x^{w}$ can be represented by the use of Horner's rule form. It becomes

$$
\begin{align*}
& \sum_{i=0}^{\lfloor(m-2) / 2\rfloor-1} L\left(a_{l}, a_{w}\right) x^{w}=\left(\cdots \left(\left(L\left(a_{m-1}, a_{m-2}\right) x^{2}+L\left(a_{m-3}, a_{m-4}\right)\right) x^{2}+\right.\right.  \tag{9}\\
& \left.\left.L\left(a_{m-5}, a_{m-6}\right)\right) x^{2}+\cdots+L\left(a_{m-2\lfloor([m-2) / 2\rfloor-1)-1}, a_{m-2\lfloor((m-2) / 2\rfloor-1)-2}\right)\right) x^{2} .
\end{align*}
$$

The product of $L\left(a_{l}, a_{w}\right)$ and $x^{2}$ is required module $F(\mathrm{x})$ that can be reduce polynomial degree by a module lookup table $M$. The lookup table $M$ is by making the polynomial $F(\mathrm{x})$. An irreducible polynomial $F(\mathrm{x})$ is a trinomial $x^{m}+x^{k}+1$ or a binary pentanomial $x^{m}+x^{k}+x^{k 1}+x^{k 2}+1$ (Hankerson authors, 2004). First, the modulo operation with lookup table is in term of the irreducible polynomial $F(x)=f_{m} x^{m}+f_{k} x^{k}+f_{k 1} x^{k 1}+f_{k 2} x^{k 2}+1$, where $f_{i}$ is belong to $\{0,1\}$, for computing modulo polynomial. Let $f=f_{k} X^{k}+f_{k 1} X^{k 1}+f_{k 2} X^{k 2}+1$ are the value of polynomial. Scheme 1, in step 2 needs table $M$ to make $L$ dynamic lookup table. The table $M$ is as shown in Table 1.

Note that, the reduction table method requires that $m-k \geq w b$, where $w b$ is the number of terms polynomial in polynomial $A(x)$. If $m-k$ is less than $w b$, the reducing polynomial $f$ to make lookup table cause polynomial $B$ is dependent. It can't use lookup table $M$ for reducing the polynomial degree. The pre-computing reduction table is shown in Table 1.

Table 1: Pre-computing reduction $M$ table

| $\mathrm{M}\left[\mathrm{a}_{\mathrm{i}}, \mathrm{a}_{\mathrm{j}}\right]$ | reduction f | Result |
| :---: | :---: | :---: |
| $\mathrm{M}[0,0]$ | $(0 \mathrm{f} \ll 1)+0 \mathrm{f}$ | 0 |
| $\mathrm{M}[0,1]$ | $(0 \mathrm{f} \ll 1)+1 \mathrm{f}$ | f |
| $\mathrm{M}[1,0]$ | $(1 \mathrm{f} \ll 1)+0 \mathrm{f}$ | $\mathrm{f} \ll 1$ |
| $\mathrm{M}[1,1]$ | $(1 \mathrm{f} \ll 1)+1 \mathrm{f}$ | $\mathrm{M}[1,0]+\mathrm{M}[0,1]$ |

The lookup table $M$ can be reduced polynomial degree, the leader coefficient of polynomial in degree $m-1$ multiplied by $x$ is become of degree $m$ that meaning needs the table $M$ to reduce the polynomial degree. The multiplication by $x$ is represented as binary form shift left one position (i.e., $B=1101, B \ll 1=1010$ ). The terms $a_{i} B x+a_{j} B$ can combinations of two bits for computing dynamic lookup table in Table 2.

Table 2: Two terms dynamic Lookup table

| $\mathrm{L}[$ ai, aj $]$ | aiBx $\operatorname{modF}(\mathrm{x})+\mathrm{ajB}$ | Result |
| :--- | :---: | :---: |
| $\mathrm{L}[0,0]$ | $0 \mathrm{Bx}+0 \mathrm{~B}$ | 0 |
| $\mathrm{~L}[0,1]$ | $0 \mathrm{Bx}+1 \mathrm{~B}$ | B |
| $\mathrm{~L}[1,0]$ | $1 \mathrm{Bx}+\mathrm{M}[0, \mathrm{bm}-1]+0 \mathrm{~B}$ | $(\mathrm{~B} \ll 1)+\mathrm{M}[0, \mathrm{bm}-1]$ |
| $\mathrm{L}[1,1]$ | $1 \mathrm{Bx}+\mathrm{M}[0, \mathrm{bm}-1]+1 \mathrm{~B}$ | $\mathrm{~L}[1,0]+\mathrm{L}[0,1]$ |

Where $b_{m-1}$ is leader of the polynomial $B=\left(b_{m-1}, b_{m-2}, \cdots+b_{0}\right)$. Let $U=L\left(a_{l}, a_{w}\right)$ be binary vector $U=\left(u_{m-1}, u_{m-2}, \ldots, u_{0}\right)$, the $L\left(a_{l}, a_{w}\right) x^{2} \bmod F(x)$ can be represented as $U \ll 2+M\left(u_{m-1}, u_{m-2}\right)$, where $U \ll 2$ is left-shift operation the binary vector (i.e., $\left.U \ll 2=\left(u_{m-3}, u_{m-4}, \ldots, u_{0}, 0,0\right)\right)$. Consequently, the scheme of multiplication for computing the two terms polynomial called Scheme 1 , is proposed as follows:

Scheme 1: Multiplication Using Look up Table (LUT) Computation

1) Compute $q \equiv m \bmod 2$, where $m$ is the element of bit sizes.
2) Set the initial message $C$ to be zero.
3) To make reduction $M\left(a_{l}, a_{w}\right)$ table as shown in Table 1
4) Computing $L\left(a_{l}, a_{w}\right)$ table as shown in Table 2.
5) for $i=0$ to $\left\lfloor\frac{(m-2)}{2}\right\rfloor-1$ do
6) Read bits $a_{m-2 i-1}$ and $a_{m-2 i-2}$ from the polynomial $A(x)$
7) $U \leftarrow C+L\left(a_{m-2 i-1}, a_{m-2 i-2}\right)$
8) $C \leftarrow(U \ll 2)+M\left(u_{m-1}, u_{m-2}\right)$
9) end for $i$
10) 

$U \leftarrow C+L\left(a_{q+1}, a_{q}\right)$
11) $C \leftarrow(U \ll q)+M\left(q u_{m-1}, q u_{m-2}\right)+q \cdot a_{0} B$

Scheme 1, the lookup table require the memory of the size $2^{2} \cdot m$ bits. For example, the $G F\left(2^{163}\right)$ requires 4 LUTs. Those tables can be dynamic pre-calculated and can then be stored in memory. The simplest approach is to use a pair of ( $a_{l}, a_{w}$ ), where $a_{l}$ and $a_{w}$ are bit value, for evaluating multiplication in LUTs derived from Scheme 1 , which requires only 80 iterations for evaluating a multiplication.

If a three-term polynomial $a_{l} x^{2}+a_{w} x+a_{x}$ is designed for computing multiplication, $q \equiv k$ mod 3 , It can be divided into $\left\lfloor\frac{(m-3)}{3}\right\rfloor$ unites into $A(x)$. In equation (1) can be replaced by following a three-term polynomial for multiplication. It is called Scheme 2 as follows:

## Scheme 2: Multiplication LUT Computation

1) Compute $q \equiv m \bmod 3$, where $m$ is the element of bit sizes.
2) Set the initial the value of $C$ to be zero.
3) To make reduction $M\left(a_{l}, a_{w}, a_{x}\right)$ table as shown in Table 3
4) Computing $L\left(a_{l}, a_{w}, a_{x}\right)$ table as shown in Table 4.
5) for $i=0$ to $\left\lfloor\frac{(m-3)}{3}\right\rfloor-1$ do
6) Read elements $a_{m-3 i-1}, a_{m-3 i-2}$ and ${ }^{a_{m-3 i-3}}$
7) $U \leftarrow C+L\left(a_{m-3 i-1}, a_{m-3 i-2}+a_{k-3 i-3}\right)$
8) $C \leftarrow(U \ll 3)+M\left(u_{m-1}, u_{m-2}, u_{m-3}\right)$
9) end for $i$
10) 
11) 

$U \leftarrow C+L\left(a_{q+2}, a_{q+1}, a_{q+3}\right)$
$C \leftarrow U \ll q+M\left(0, u_{m-1}, u_{m-2}\right)+\operatorname{sgn}(q) \cdot\left(\sum_{j=0}^{q-1} a_{j} B x^{j}\right) \bmod F(x)$
Where $\operatorname{sgn}(q)=\left\{\begin{array}{l}1 \text { if } q>0, \\ 0 \text { if } q=0 .\end{array}\right.$

Table 3: Pre-computing reduction $M$ table

| $M\left[a_{i}, a_{j}\right]$ | reduction $f$ | Result |
| :--- | :---: | :---: |
| $M[0,0,0]$ | $(0 f \ll 1)+0 f$ | 0 |
| $M[0,0,1]$ | $(0 f \ll 1)+1 f$ | $f$ |
| $M[0,1,0]$ | $(1 f \ll 1)+0 f$ | $f \ll 1$ |
| $M[0,1,1]$ | $(1 f \ll 1)+1 \mathrm{f}$ | $M[0,1,0]+\mathrm{M}[0,0,1]$ |
| $M[1,0,0]$ | $(1 \mathrm{f} \ll 2)+(0 f \ll 1)+0 f$ | $f \ll 2$ |
| $M[1,0,1]$ | $(1 \mathrm{f} \ll 2)+(0 f \ll 1)+1 \mathrm{f}$ | $M[1,0,0]+\mathrm{M}[0,0,1]$ |
| $M[1,1,0]$ | $(1 f \ll 2)+(1 \mathrm{f} \ll 1)+0 \mathrm{f}$ | $M[1,0,0]+\mathrm{M}[0,1,0]$ |
| $M[1,1,1]$ | $(1 \mathrm{f} \ll 2)+(1 \mathrm{f} \ll 1)+1 \mathrm{f}$ | $\mathrm{M}[1,0,0]+\mathrm{M}[0,1,1]$ |

Table 4: Three term dynamic lookup table

| $\mathrm{L}\left[\mathrm{a}_{1}, \mathrm{a}_{\mathrm{w}}, \mathrm{a}_{\mathrm{x}}\right]$ | $\mathrm{a}_{1} B x^{2} \operatorname{modF}(\mathrm{x})+\mathrm{a}_{\mathrm{w}} B x \operatorname{modF}(\mathrm{x})+\mathrm{a}_{\mathrm{x}} \mathrm{B}$ |
| :--- | :---: |
| $\mathrm{L}[0,0,0]$ | 0 |
| $\mathrm{~L}[0,0,1]$ | B |
| $\mathrm{L}[0,1,0]$ | $(\mathrm{B} \ll 1)+\mathrm{M}\left[0,0, \mathrm{a}_{\mathrm{m}-1}\right]$ |
| $\mathrm{L}[0,1,1]$ | $\mathrm{L}[0,1,0]+\mathrm{L}[0,0,1]$ |
| $\mathrm{L}[1,0,0]$ | $(\mathrm{B} \ll 2)+\mathrm{M}\left[0, \mathrm{a}_{\mathrm{m}-1}, \mathrm{a}_{\mathrm{m}-2}\right]$ |
| $\mathrm{L}[1,0,1]$ | $\mathrm{L}[1,0,0]+\mathrm{L}[0,0,1]$ |
| $\mathrm{L}[1,1,0]$ | $\mathrm{L}[1,0,0]+\mathrm{L}[0,1,0]$ |
| $\mathrm{L}[1,1,1]$ | $\mathrm{L}[1,0,0]+\mathrm{L}[0,1,1]$ |

Scheme 2, the lookup table require the memory of the size $2^{3} \cdot m$ bits. For example, the $G F\left(2^{163}\right)$ requires 8 data in lookup table. Those tables can be dynamic pre-calculated data to store in memory. The simplest approach is to use three for ( $a_{l}, a_{w}, a_{x}$ ), where $a_{l}, a_{w}$, and $a_{x}$ are bit value, for precomputing data in Table 3 and in Table 4. Scheme 2 used Table 3 and Table 4 operation, which requires only 53 iterations for evaluating a multiplication, however, they would be increased time to make dynamic lookup table. Characteristic two in this is case due to work described in (Gaudry authors, 2000) $m$ to be prime. According to Scheme 1 (i.e., using Table 1 and Table 2) and Scheme 2 (i.e., using Table 3 and Table 4), If the value of $m$ is odd number, the multiplications can be designed as Algorithm 1.

So, Algorithm 1 can use reducing polynomial $f(x)$ in Table 5 and $w d$-term polynomial $A(x)$ in Table 6. These precomputing the table of the size is $2^{w d}$, where $w d$ is the number of terms polynomial (i.e., $a_{w d-1} x^{w d-1}+a_{w d-2} x^{w d-2}+\cdots+a_{0}$ ). The flowchart is as shown in Figure 1.

Table 5: Pre-computing $2^{\text {wd }}$ reduction table

| $M\left[a_{w d-1}, a_{w d-2}, \ldots, a_{0}\right]$ | reduction f | Result |
| :--- | :---: | :---: |
| $M[0, \ldots, 0,0,0]$ | $0+\cdots+(0 f \ll 1)+0 f$ | 0 |
| $M[0, \ldots, 0,0,1]$ | $0+\cdots+(0 f \ll 1)+1 f$ | $f$ |
| $M[0, \ldots, 0,1,0]$ | $0+\cdots+(1 f \ll 1)+0 f$ | $f \ll 1$ |
| $M[0, \ldots, 0,1,1]$ | $0+\cdots+(1 f \ll 1)+1 f$ | $M[0, \ldots, 0,1,0]+M[0, \ldots, 0,0,1]$ |
| $M[0, \ldots, 1,0,0]$ | $0+\cdots+(1 f \ll 2)+(0 f \ll 1)+0 f$ | $f \ll 2$ |
| $M[0, \ldots, 1,0,1]$ | $0+\cdots+(1 f \ll 2)+(0 f \ll 1)+1 f$ | $M[0, \ldots, 1,0,0]+M[0, \ldots, 0,0,1]$ |
| $M[0, \ldots, 1,1,0]$ | $0+\cdots+(1 f \ll 2)+(1 f \ll 1)+0 f$ | $M[0, \ldots, 1,0,0]+M[0, \ldots, 0,1,0]$ |
| $\vdots$ | $\vdots$ | $\vdots$ |
| $M[1, \ldots, 0,0,0]$ | $(1 f \ll w d-1)+\cdots+0 f$ | $f<c w d-1$ |
| $\vdots$ | $\vdots$ | $\vdots$ |
| $M[1, \ldots, 1,0,1]$ | $(1 f \ll w d-1)+\cdots+(0 f \ll 1)+1 f$ | $M[1, \ldots, 0,0,0]+M[0, \ldots, 1,0,1]$ |
| $M[1, \ldots, 1,1,0]$ | $(1 f \ll w d-1)+\cdots+(1 f \ll 1)+0 f$ | $M[1, \ldots, 0,0,0]+M[0, \ldots, 1,1,0]$ |
| $M[1, \ldots, 1,1,1]$ | $(1 f \ll w d-1)+\cdots+(1 f \ll 1)+1 f$ | $M[1, \ldots, 0,0,0]+M[0, \ldots, 1,1,1]$ |

Table 6: $2^{w d}$-term polynomial for dynamic lookup table

| $L\left[a_{k_{w d-1}}, \ldots, a_{k_{2}}, a_{k_{1}}, a_{k_{0}}\right]$ | $a_{k_{w d-1}} B x^{w d-1} \bmod F(x){ }_{+\ldots+} a_{k_{1}} B x \bmod F(x)_{+} a_{k_{0}} B$ |
| :--- | :---: |
| $L[0, \ldots, 0,0,0]$ | 0 |
| $L[0, \ldots, 0,0,1]$ | $B$ |
| $L[0, \ldots, 0,1,0]$ | $(B \ll 1)+M\left[0, \ldots, 0,0, b_{m-1}\right]$ |
| $L[0, \ldots, 0,1,1]$ | $L[0, \ldots, 0,1,0]+L[0, \ldots, 0,0,1]$ |
| $L[0, \ldots, 1,0,0]$ | $(B \ll 2)+M\left[0, a_{m-1}, a_{m-2}\right]$ |
| $L[0, \ldots, 1,0,1]$ | $L[0, \ldots, 1,0,0]+L[0, \ldots, 0,0,1]$ |
| $L[0, \ldots, 1,1,0]$ | $L[0, \ldots, 1,0,0]+L[0, \ldots, 0,1,0]$ |
| $\vdots$ | $\vdots$ |
| $L[1, \ldots, 0,0,0]$ | $(B \ll w d-1)+M\left[0, b_{m-1}, b_{m-2}, \ldots, b_{m-w d}\right]$ |
| $\vdots$ | $\vdots$ |
| $L[1, \ldots, 1,0,1]$ | $L[1, \ldots, 0,0,0]+L[0, \ldots, 1,0,1]$ |
| $L[1, \ldots, 1,1,0]$ | $L[1, \ldots, 0,0,0]+L[0, \ldots, 1,1,0]$ |
| $L[1, \ldots, 1,1,1]$ | $L[1, \ldots, 0,0,0]+L[0, \ldots, 1,1,1]$ |

Algorithm 1: Multiplication dynamic Lookup table computation GFM ( $\boldsymbol{A}, \boldsymbol{B}$ ):

1) Compute $q \equiv m \bmod w d$
2) Set the initial the value of $C$ to be zero
3) To make reduction $M\left(a_{k_{0}}, a_{k_{1}}, \ldots, a_{k_{w d-1}}\right)$ table as shown in Table 5
4) To make dynamic lookup table $L\left(a_{k_{0}}, a_{k_{1}}, \ldots, a_{k_{w d-1}}\right)$ as shown in Table 6
5) for $i=0$ to $\left\lfloor\frac{(m-w d)}{w d}\right\rfloor-1$ do
6) Read elements $a_{m-3 i-1}, a_{m-3 i-2}, \ldots$, and $a_{m-3 i-w d}$
7) 

$U \leftarrow C+L\left(a_{m-3 i-1}, a_{m-3 i-2}+\cdots+a_{k-3 i-w d}\right)$
8)
$C \leftarrow(U \ll w d)+M\left(u_{m-1}, u_{m-2}, \ldots, u_{m-w d}\right)$
9) end for $i$
10) $U \leftarrow C+L\left(a_{q+w t-1}, a_{q+w t-2}, \ldots, a_{q}\right)$
11)
$C \leftarrow U \ll q+M\left(0, . ., 0, u_{m-1}, u_{m-2}, \ldots, u_{m-q}\right)+\left(\sum_{j=0}^{q-1} a_{j} B x^{j}\right) \bmod F(x)$
12) Return $C$


Figure 1: the flowchart is large size multiplication operation in $G F\left(2^{m}\right)$

## 4. RESULTS AND DISCUSSIONS

Regarding the efficiency of the proposed algorithm for encryption and decryption evaluation for EEC, a software simulation is performed on an Intel $\circledR^{\circledR}$ Core $^{\text {TM }}$ i5 at 3.07 GHz Windows 7 PC using C++ program. The element in $G F\left(2^{m}\right)$ is implemented using an unsigned character data type. For any two elements in the finite field, the multiplication is described in (Genser et al. 2009), and the addition operation is implemented directly by using the bitwise C++ XOR. The multiplications size of $m$ evaluating time of the 163, 233, 283, 409, and 571 listed in Table 7. The computational time of these methods over 100,000 times of message computations is listed in Table 8.

Table 7: The multiplication of the making table time with different finite filed

| m | wd-term polynomial, time (ns) |  |  |  |  |  |  |
| :--- | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | 1 | 2 | 3 | 4 | 5 | 6 | 7 |
| 163 | 0 | 1.5 | 1.6 | 1.5 | 6.3 | 10.9 | 20.3 |
| 233 | 0 | 1.5 | 1.6 | 3.1 | 6.2 | 12.5 | 23.4 |
| 283 | 0 | 1.5 | 1.6 | 4.7 | 7.8 | 15.6 | 31.2 |
| 409 | 0 | 1.5 | 3.1 | 4.7 | 10.9 | 20.3 | 39.0 |
| 571 | 0 | 1.6 | 3.1 | 6.2 | 12.5 | 25.0 | 49.9 |

Table 8: The multiplication of the looping time with different finite filed

| m | wd-term polynomial, time (ns) |  |  |  |  |  |  |
| :--- | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | 1 | 2 | 3 | 4 | 5 | 6 | 7 |
| 163 | 71.7 | 34.4 | 23.3 | 20.6 | 13.9 | 11 | 10.9 |
| 233 | 143.5 | 72 | 48.3 | 35.9 | 29.7 | 24.9 | 21.8 |
| 283 | 201.0 | 8.3 | 65.4 | 48.3 | 40.6 | 32.7 | 24.9 |
| 409 | 449.2 | 232.5 | 148.3 | 113.9 | 89.0 | 74.8 | 62.4 |
| 571 | 703.5 | 348.1 | 235.2 | 174.8 | 138.8 | 115 | 101.4 |

Table 9: The computing time of the multiplication with different finite field

| m | Russian Peasant method | Diversity look table method wd-term polynomial, time (ns) |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | 1 | 2 | 3 | 4 | 5 | 6 | 7 |
| 163 | 69.5 | 71.7 | 35.9 | 24.9 | 22.1 | 20.2 | 21.9 | 31.2 |
| 233 | 139.2 | 143.5 | 73.5 | 49.9 | 39.0 | 35.9 | 37.4 | 45.2 |
| 283 | 194.9 | 201.0 | 99.80 | 67.0 | 53.0 | 48.4 | 48.3 | 56.1 |
| 409 | 435.7 | 449.2 | 234.0 | 151.4 | 118.6 | 99.9 | 95.1 | 101.4 |
| 571 | 682.4 | 703.5 | 349.7 | 235.5 | 181.0 | 151.3 | 140.0 | 151.3 |

Table 10: The memory usage

| m | Russian Peasant method | Diversity look table method |  |  |  |  |  |  |
| :--- | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | needs memory size (bytes) | wd-term polynomial needs memory size (bytes) |  |  |  |  |  |  |
|  |  | 1 | 2 | 3 | 4 | 5 | 6 | 7 |
| 163 | 0 | 0 | 163 | 326 | 652 | 1304 | 2608 | 5216 |
| 233 | 0 | 0 | 223 | 446 | 892 | 1784 | 3568 | 7136 |
| 283 | 0 | 0 | 283 | 566 | 1132 | 2264 | 4528 | 9056 |
| 409 | 0 | 0 | 409 | 818 | 1636 | 3272 | 6544 | 13088 |
| 571 | 0 | 0 | 571 | 1142 | 2284 | 4568 | 9136 | 18272 |

Algorithm 1, the multiplication compute with $m=163$ and $w d=5$ that can be approximately $70 \%$ faster than Russian Peasant method. For some field, the computations for the multiplication had been executed over 100,000 data for testing. The large field $m=163$ and $w d=2$ the proposed is used to compute multiplication, which requires $4+83=87$ Left Shift (i.e., <<) operand and $1+83^{*} 2=164$ XOR (i.e., $\wedge$ ) operand. The inverse computing process is required the number of multiplications and square are the number of $m$. The multiplication can be applying Algorithm 1 to evaluate scalar multiplication in Elliptic Curve. Algorithm 1 reducing time performance is better than Russian Peasant method that shown in Figure 2.


Figure 2: The multiplication of $m$ bits operation in finite field

## 5. CONCLUSIONS AND RECOMMENDATIONS

In this paper, dynamic lookup table method using encryption and decryption in the ECC is presented. In Figure 2, Algorithm 1 is actually faster than Russian Peasant method, where $w d>1$ in all instances $m$ bits. The proposed multiplication method also can perform quickly inverse operation. If memory consumption in embedded systems is an acceptable range, the proposed method can be readily adaptable for speeding up and memory used the point multiplication in ECC. Thus, Algorithm 1 can use in different the value of $w d$ to divide the polynomial $A(x)$ for encryption and decryption that can save a lot of the memory in Table 10 when the value of $w d$ is small. It is evident that Algorithm 1 is really suitable for software applications in embedded system.

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## CONFLICT OF INTEREST

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